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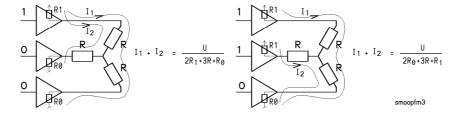
Open-loop control for smoovy motors

RMB smoovy motors are synchronous motors which can be controlled open-loop with a better efficiency using the PFM scheme proposed by O. Matthey. The basic idea is to smoothen the transitions in order to achieve close to the ideal sinusoid excitation for which synchronous motors are designed. The additional difficulty with miniature smoovy motors is that rotor inertia is very low compared to the magnetic forces. Careful experiment has shown that, with the PFM software constraints, trapezoidal transitions perform better. PFM is less time-consuming than PWM and is easier to implement on simple microcontrollers like those of the Microchip PIC family.

1. Interfacing

The smoovy, like all three-phase synchronous motors, has three coils around a rotor which is just the best possible magnet. The rotating electromagnetic field drive? the rotor with a phase shift that generate the active torque. If one can control this phase shift, as brushless motors with Hall sensors and analog electronics do, one must overpower the motor to be sure not to loose steps, and have the motor stop. Open-loop control will, however, always be the only way to go with the smallest motors.

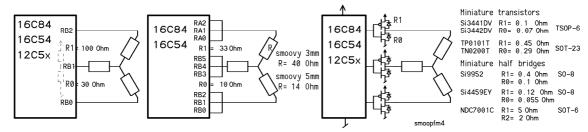
The coil resistance of the 3mm is about 40 Ohm, which is a major advantage since it can be directly powered by some microcontrollers. Smaller motors do not have such a high resistance. The more powerful 5 mm smoovy has a 14 Ohm coil resistance. Power amplifiers have a resistance toward the supply or the ground which will define the efficiency of the system. Coils are never controlled individually. They are connected in a star configuration, with a common point which may provide an indication on the current. Triangle connections are seldom used.



smoopfm3

Fig. 1 Star connection of the motor coils

The amplifiers are easy to build with miniature low resistance MOS transistors (figure 2). Amplifiers are not required for the 3mm smoovy, if a reduced torque can be accepted. Connecting outputs together reduces the internal equivalent PIC resistance and is of course preferable, but the outputs for a given coil must be on the same port.



smoopfm4

Fig. 2 Power amplifiers and typical resistance values

2. Fixed frequency smoovy control

A table defines the sequence of steps (figure 3). If the PIC has no other task to perform, a simple delay loop defines the period between pulses, that is the rotation speed. Due to its low inertia, the smoovy will start at rather high frequency (about 1000 RPM). But for some lower speed, it may overshoot and not work correctly.

Half-steps are possible if the power amplifiers have an "enable" input. The processor must in this case generate 6 signals, and there are 12 steps. The torque is less regular, however, due to a lower total current when a coil is disconnected during a half-step. We will no longer consider half-steps.

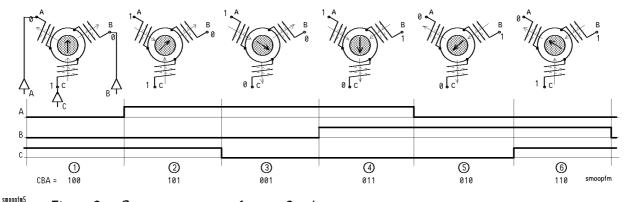


Fig. 3 Step sequence for a 3-phase motor

The software corresponding to figure 3 is quite simple. In the example below, the 3 phases are connected to the three low bits of PortB. A pointer onto the step sequence table

```
CALM source program with SmileNG editor- Test smoovy
Microchip source program
                                                          .proc
                                                                   16c84
                                                                             ; 4MHz clock
      LIST
                 P=16C84
                                                          Variables Registers
                                                                            .Loc
C1
        EQU
                 0xC
                                                                   .16
                                                         C1:
                                                                                      ; local variables (counters, e
                                                                             1
        EQU
                 0xD
                                                                   .16
                                                         C2:
MOTPOS EQU
                 0xE
                                                         MotPos:
                                                                   .16
                                                                                      ; used by motor loop (motor
                                                          Variables PortB
                                                                        16C84
; PORTB 16C84
                                                                             ; RBØ Pin 7
                                                         bM1
                                                                   = 0
BM1
        EQU
                 0
                                                         bM2
                                                                              Pin 8
                                                                   = 1
BM2
        FOU
                 1
                                                         ьмз
                                                                   = 2
                                                                              Pin 9
RM3
        EQU
                                                         mDirB
                                                                   = 2'0000000
                                                                                      ; all outputs
        EQU
MDIRB
                 ØBØØØØØØØ
                                                                Program
                                                         Program:
; PROGRAM
                                                          .Loc
                                                                   0
                                                         Begin:
BEGIN
                                                                   Move
                                                                             #mDirB,W; Direction out
      MOVLW
                 MDIRB
                                                                             W, TrisB
                                                                   Move
                 PORTR
      TRIS
                                                         Per
                                                                              10ms --> 60ms/turn 16,6t/s 1000t
                                                                   = 100
PER EQU
                 100
                                                                     min 20 for unloaded motor
                                                                   Clr
                                                                            MotPos
                                                                                      ; motor position index
                                                         Loop:
                 MOTPOS
LOOP CLRF
                                                          M$:
                                                                   Move
                                                                             #Per,W
      MOVLW
                 PER
                                                                   Call
                                                                             Delay
      CALL
                 DELAY
                                                                   Move
                                                                             MotPos, W
      MOVF
                 MOTPOS, W
                                                                   Inc
                                                                             MotPos
                 MOTPOS
      INCF
                                                                   Call
                                                                             TaForward
                 TAFORWARD
      CALL
                                                                   Move
                                                                             W, PortB
      MOVWF
                 PORTB
                                                                   Move
                                                                             #6,W
                                                                                      ; 6 phases per turn
      MOVLW
                                                                             W, MotPos, W
                                                                   Sub
                                                                                        ; Compare #6, Mot Pos
                 MOTPOS, W
      SUBWF
                                                                   Skip, EQ
      BTFFS
                 STATUS,3
                                                                   Jump
      GOTO
                                                                   Jump
                                                                             Loop
                 LOOP
      GOTO
                                                          Routine Delay Delay multiple of 100\mus (4MHz clock)
                                                                W delay 0, 0,1 ... 25,5 ms
C1 C2 W
                                                           in:
                                                                   Move
                                                         Delay:
                                                                             W,C1
DELAYMOVWF
                 C1
                                                                                      ; loop 100\mu s
                                                          A$:
                                                                   Move
                                                                             #32,W
      MOVLW
                 32
                                                                             W,C2
                                                                   Move
      MOVWF
                 C2
                                                             B$:
                                                                   DecSkip, EQ C2
В
      DECFSZ
                 C2
                                                                   Jump
      GOTO
                 В
                                                                   DecSkip, EQ C1
      DECFSZ
                 C1
                                                                   Jump
      GOTO
                                                                   Ret
      RETURN
                                                          Constant Tables Motor
                                                         TaForward:
                                                                                      ; Motor phases on bM3 bM2 b
                                                                             W,PCL
                                                                   Add
TAFORWARD
                                                                            #2'100,W
                                                                   RetMove
                                                                                                   ; MotPos = 0
      ADDWF
                                                                             #2'110,W
                                                                   RetMove
      RETLW ØB100
                                                                            #2'010,W
                                                                   RetMove
      RETLW ØB110
                                                                   RetMove
                                                                            #2'011,W
      RETLW ØBØ1Ø
                                                                   RetMove
                                                                            #2'001,W
      RETLW ØBØ11
                                                                            #2'101,W
                                                                   RetMove
                                                                                                   ; MotPos = 5
      RETLW ØBØØ1
```

is initiatized DBAD 1zero, but is incremented before accessing the data. Hence, the first position in the table is never accessed. At each step, the next value in the table is taken. When

the pointer has reached position 6, it is reinitialized to zero.

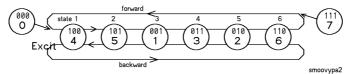
3. Reorganizing the table

Instead of a table organized by consecutive states, a table giving the next excitation according to the present one is more efficient, since there is no modulo-6 counter to manage. The variable which corresponds to the excitation status of the motor is "Excit". It could even be read on the motor port itself, but since the PIC reads the output value and not the internal output register, it is not recommended, especially in the case of capacitive loads. The "Excit" variable is initialized at zero, and the next valid value is found automatically in the table.

uuto	macrouny	iii tiic tabic.				
			Unidirectional co	ontrol		
BEG			Beg:			
	CLR	EXCIT	Clr	Excit		
LOOP	•		Loop:			
	MOVF	EXCIT,W	Move	Excit, W		
	CALL	TAFORWARD	Call	TaForward		
	MOVWF	EXCIT	Move	W,Excit		
			; Superpose othe		tten on the port	
	MOVWF	6	Move	W, PortB	·	
	GOTO	M_Ø	Jump	Loop		
			.macro d	; prepare da	ta table	
			RetMove	#2′%1,W		
TAFORWARD			. endmacro			
	ANDLW	Øb111				
	ADDWF	2	300000000			
	RETLW 0b001		Routine Table Mo	Routine Table Motor		
	RETLW	0b101	TaForward:			
	RETLW	0b011	And	#2′111,W		
	RETLW	0b001	Add	W,PCL		
	RETLW	0b110	d	øó1	; any initial valid value	
	RETLW	0b100	d	101	; 001> 101 bM2	
	RETLW	0b010			bM1 bMØ	
			d	Ø11	; 010> 011	
			d	001	; 011> 001	
			d	110	: 100> 110	
			d	100	; 101> 100	
			d	010	; 110> 010	

4. Bidirectional control

Changing direction may be implemented with two separate tables, with the selection of the table being made according to a variable or a flag. It is simpler, though, to have a single table which includes two 3-bit excitation values corresponding to forward and backward rotation (figure 4).



smoovypa2

Fig. 4 Bidirectional transition diagram

The corresponding excitation table can be written explicitly if a macro is defined to get the correct set of bits at the right place. Due to the existence of a Swap instruction, the forward value is placed in the upper 4-bits and the backward in the lower 4-bits.

The program loop is quite simple: according to the direction bit "bBack" in a variable named here "MotorStatus" (could be the same as "Excit", where several bits are free), swapping occurs and the motor can change direction at any state (if the motor speed allows it).

```
Bidirectional control (first solution)
                                                                            #1,W
      MOVLW
                                                               Move
      MOVWF
                EXCIT
                                                                            W, Excit
                                                               Move
                                                                                           ; Initialization
LOOP
                                                          Loop:
                EXCIT, W
      MOVE
                                                               Move
                                                                            Excit.W
      CALL
                TABIDIR
                                                               Call
                                                                            TaBidir
      MOVWF
                EXCIT
                                                               Move
                                                                            W, Excit
      BTFSS
                MOTORSTATUS . bBACKWARD
                                                               TestSkip,BS
                                                                            MotorStatus:#bBack
      SWAPE
                EXCIT
                                                               Swap
                                                                            Excit
      MOVF
                EXCIT, W
                                                               Move
                                                                            Excit, W
      ANDLW
                B'111
                                                               And
                                                                            #2'111,W
                PORTB
                                                         ; Superpose other bits to be written on the port
      MOVWF
                LOOP
                                                                            W, PortB
      GOTO
                                                               Move
                                                               Jump
                                                                            Loop
                                                                                           ; prepare a table 0xxx0v
                                                                               dd
                                                         . macro
                                                                        #%1*(2**4)+%2,W
                                                               RetMove
TABIDIR
                                                         . endmacro
      ANDLW
                B'111'
      ADDWF
                                                         TaBidir:
                2
                                                                            #2′111,W
                1*16+1
      RETLW
                                                               And
                                                                            W,PCL
      RETLW
                3*16+5
                                                               Add
                                                                                          ; not supposed to get th
      RETLW
                6*16+3
                                                               dd
                                                                            1,1
                                                                                          ; forward 1 ->3 /backw
      RETLW
                2*16+1
                                                               dd
                                                                            3,5
      RETLW
                5*16+6
                                                                            6,3
                                                               dd
      RETLW
                1*16+4
                                                               dd
                                                                            2,1
      RETLW
                 4*16+2
                                                               dd
                                                                            5,6
      RETLW
                0×16+0
                                                               dd
                                                                            1,4
                                                               Ьb
                                                                            4,2
                                                               dd
                                                                            0,0
```

It is in fact faster to have two consecutive tables, and switch according to the "bBack" bit, stored as bit 3 within "Excit".

```
Bidirectional control (second solution
LOOP
                                                           Loop:
      MOVF
                 EXCIT, W
                                                                  Move
                                                                               Excit, W
      CALL
                 TABIDIR
                                                                  Call
                                                                               TaBidir
      MO\/WE
                                                                               W. Excit
                 FXCIT
                                                                  Move
      ANDLW
                 B'111
                                                                  And
                                                                               #2'111,W
                                                            ; Superpose other bits to be written on the port
      MOVWF
                 6
                                                                 Move
                                                                               W, PortB
      GOTO
                 LOOP
                                                                  Jump
                                                                               Loop
                                                                                               ; prepare a table 0xxx0y
                                                                                   dd
                                                            . macro
                                                                  RetMove
                                                                           #%1,W
                                                            . endmacro
TABIDIR
                                                           TaBidir:
      ANDLW
                 B'1111'
                                                                               #2'1111.W
                                                                  And
                                                                               W,PCL
      ADDWF
                                                                  Add
      RETLW
                                                                                               ; not valid, arbitrary ne
                                                                  dd
                                                                                               ; forward 1 \rightarrow 3
      RETLW
                 3
                                                                  dd
                                                                               3
      RFTI W
                 6
                                                                               6
                                                                  dd
      RETLW
                 2
                                                                  dd
                                                                               2
      RETLW
                 5
                                                                               5
                                                                  dd
      RETLW
                                                                  dd
                                                                               1
                                                                  dd
                                                                               4
      RETLW
                 4
      RETLW
                                                                  dd
                                                                               1
                                                                                               ; not valid
                                                            ; When bBack bit is active
                                                                                               ; not valid
      RETLW
                 1
                                                                  dd
                                                                               1
                                                                                               ; backward 1 -> 5
      RETLW
                 5
                                                                  dd
                                                                               5
      RETLW
                 3
                                                                  dd
                                                                               3
      RETLW
                                                                               1
                                                                  dd
      RETLW
                 6
                                                                  dd
                                                                               6
      RETLW
                 4
                                                                  dd
                                                                               4
      RETLW
                 2
                                                                  dd
                                                                               2
      RETLW
```

5. Synchronous programming

Getting step delays from a waiting loop is only possible for simple test programs. Interrupts are not efficient, if supported, with microcontrollers. The solution is to do synchronous programming, where all the operations the program has to do are selected within a loop of constant duration. Motors and devices with precise timings are controlled every loop or every n loops. Other tasks are scheduled according to the previous task, to their priority, or to the raising of flags requesting operation. More details are given in [Nicoud98].

snoophecolleplan = 1il: SMOOPFM6
Fig. 5 Synchronous programming action sequence

Rewriting the previous program for the step control in the 100 μ s loop gives:

	· '	. •	. •
LOOP		Loop:	; Excecuted every x μ s
DECFSZ	STEPPERIOD	DecSkip,EQ	StepPeriod
GOTO	DOSTEP	Jump	DoStep
MOVLW	PERIOD	Move	#Period,W
MOVWF	STEPPERIOD	Move	W,StepPeriod
GOTO	NEXT	Jump	Next
DOSTEP		DoStep:	
MOVF	EXCIT,W	Move	Excit, W
CALL	TAFORWARD	Call	TaForward
MOVWF	6	Move	W, PortB
NEXT		Next:	; continuation

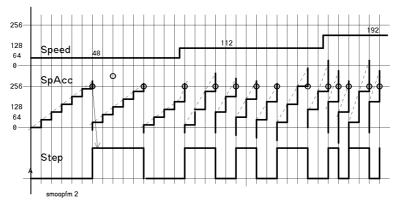
More information on synchronour programming can be found on www.didel.com/DopiSync.pdef.

6. Variable frequency control

In order to modify the rotation speed of the motor, one usually alters the delay between steps, which is the period. Linear period duration modification provides a non-constant acceleration, but the effect is usually insignificant.

A better and frequently simpler solution is to define a speed variable which represents the frequency of the steps. This has the advantage of allowing synchronous programming. At every loop (e.g. every 200 μ s), the speed "Speed" is added to a counter "SpAcc". If the counter overflows, a step must be made. With an 8-bit counter, minimum speed (= 1) corresponds to a 51.2 ms step period (200 μ s loop), which is about 3 turns per second. Theoretical maximum speed is 255 for a 200 μ s period (but there is a 400 μ s step every 256 steps), which is about 60,000 RPM.

With both the period approach and the speed approach, the digitalization problem is bad at high speed (compared to processor speed). It is safer to use the available faster PIC or Scenix processors and work with 16-bit precision. Our examples will be given with 8 bits, assuming a speed value between 1 and 50 (20% jitter).



snoopin2 Fig. 6 Step frequency proportional to "Speed" variable

One may hesitate to clear the SpAcc register when it overflows. The jitter will be reduced, but this jitter generates an average speed with a finer resolution. If SpAcc is reset at every step, several increment values are ignored. For instance, there is no change in speed between 32 and 37, since $32 \times 8 = 256$ and $37 \times 7 = 259$.

If the minimum speed value is too fast for some application, it is easy to increase the synchronous loop duration, or to use a 16-bit SpAcc register, with the advantage of a wide speed range with a short loop producing minimal jitter.

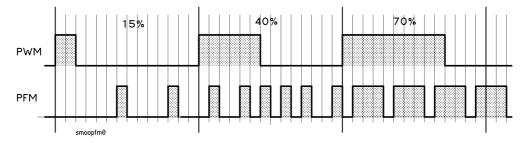
```
; Excecuted every 200 \mu s (compare with program module on
OP1
     MOVF
                 SPEED, W
                                                           page xx)
      ADDWF
                 SPACC
                                                           Op1: Move
                                                                              Speed, W
      BTESS
                                                                 Δdd
                                                                              W,SpAcc
                 3.0
                                                                 Skip, CS
     GOTO
                 OP2
                 SPACC
                                                                                              ; No step if no overflow
      CLRF
                                                                 Jump
                                                                              Op2
     MOVF
                 EXCIT, W
                                                                                SpAcc
                                                                   Clr
                                                                                                ; optional
                 TAFORWARD
      CALL
                                                                 Move
                                                                              Excit, W
      MOVWF
                 EXCIT
                                                                 Call
                                                                              TaForward
      MOVWF
                                                                 Move
                                                                              W, Excit
OP2
     ; continuation
                                                                              W.PortB
                                                                 Move
                                                           Op2:
                                                                               ; continuation
```

This program module does not execute in the same amount of time when no step is executed. If required, it is easy to add a jump and several no-op instructions.

7. Variable frequency with PFM transitions

A major difficulty with synchronous motors is that they should be powered with sine waves. At high speed (10000 RPM for an unloaded 3mm smoovy), the motor's inertia smooths things out, and so square waves are acceptable. O. Matthey has studied the motor's dynamics and proposes the PFM scheme with trapezoidal ramps as the best solution on the PIC for smoothing rotational movement. Even with PWM hardware, smoothing the steps of a stepping/synchronous motor at smoovy speed would not be easy.

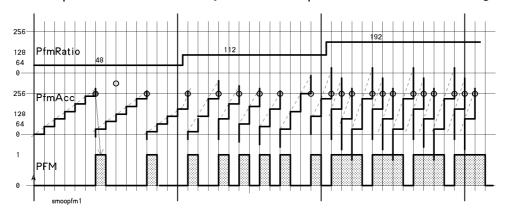
PWM is well known: motor phases receive pulses with a fixed period but a variable width (figure 7a). PFM, on the other hand, uses a fixed positive or negative pulse length and a variable repetition period (figure 7b). When not implemented in hardware (no PIC does this), software PWM implementations require two counters and are tricky to insert in a synchronous programming concept (difficulty also arises when the PWM ratio is 0 or 1).



smoopfm0

Fig. 7 PWM versus PFM

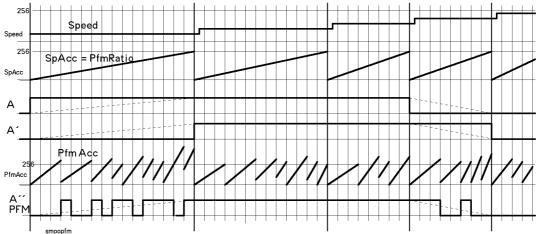
PFM is easy to implement and is specially suited for synchronous programming or when a fast timer interrupt (e.g. 200 μ s) is programmed: the PFM ratio is a value added at every interrupt to a counter PfAcc. When this counter overflows (carry set), the next value is sent to the motor phase. Otherwise (carry clear), the previous value is taken (figure 8).



smoopfm1

Fig. 8 PFM generation

The next figure shows approximately how one phase will be switched at every transition, in the case of a very fast speed increase. The SpAcc value is taken as a PfmRatio variable (trapezoidal waveform) and added to the PfAcc counter. When this counter overflows, the next step value is sent to the motor phase.



smoopfm 9 PFM interpolation between phase transition Fig. ; End of program initialization MOVLW INISPEED #IniSpeed,W Move MOVWF **SPEED** Move W, Speed CLRF SPACC Clr SpAcc CLRF **PFMACC** PfmAcc Clr ; Endless synchronous loop LOOP Loop: ; Fixed timing task Execute a PFM microstep 26 µs duration (16C84/ SPACC, W MOVF Move SpAcc, W PEMACC bbA W, PfmAcc ADDWF MOVF EXCIT, W Move Excit, W Skip,CS **BTFSS** 3,0 **SWAPF** EXCIT, W Swap ; CC, takes previous val Excit.W #2′111,W And ANDLW B'1111' ; Superpose other bits to be written on the port MOVWF W, PortB 6 Move ; next motor step? SPEED, W MOV/F Speed, W Move **ADDWF** SPACC Add W,SpAcc ; PfmRatio **BTFSS** 3,0 Skip, CS GOTO NOSTEP NoStep\$ Jump ; optional Clr SpAcc MOVF EXCIT, W Clr PfmAcc ; optional CALL **TAFORWARD** Move Excit, W MOVWF EXCIT Call TaForward Move W, Excit GOTO OP2 Jump Op2 NOSTEP NoStep\$: ; Duration compensatio MOVLW #3,W 3 Move MOVWF C1 Move W,C1 **DECFSZ** C1 A\$: DecSkip, EQ C1 GOTO Α Jump A\$ OP2 Op2: ; e.g. control a second motor at different speed ; ... GOTO LOOP Jump Loop Module Motor table (unidirectional) **TAFORWARD** prepare a table 0xxx0y . macro ANDLW B'1111 #%1*(2**4)+%2,W RetMove **ADDWF** . endmacro RETLW 0×16+1 TaForward: RETLW 1*16+5 And #2'111,W RETLW 2*16+3 Add W.PCL RETLW 3*16+1 0,1 dd RETLW 4*16+6 dd 1,5 ; "present, next" motor e RETLW 5*16+4 dd 2,3 RETLW 6*16+2 3,1 dd END Ьb 4,6 5,4 dd

dd

6,2

J.D. Nicoud, August 1998